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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number	09/935,779
				Filing Date	August 24, 2001
				First Named Inventor	Frampton E. ELLIS
				Art Unit	2153
				Examiner Name	A. Strange
Sheet	1	of	2	Attorney Docket Number	313449-P0008

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US -5,701,507	12/23/1997	Bonneau, Jr., et al.	
		US -6,219,627	4/17/2001	Bonneau, et al.	
		US -5,497,465	3/1/1996	Chin et al.	
		US -5,943,421	8/1/1999	Grabon, Robert J.	
		US -5,600,597	2/1/1997	Kean et al.	
		US -5,754,766	5/1/1998	Shaw et al.	
		US -5,872,987	2/1/1999	Wade et al.	
		US -5,136,708	8/1992	LaPourtre et al.	
		US -5,696,902	12/1997	Leclercq et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP 0 853 279	7/1998	Nagaratnam et al.		
Examiner Signature				Date Considered		

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Notice of Allowance issued in Application No. 09/884,041 mailed October 9, 2008	
		Wilkinson, Barry et al., "Parallel Programming: Techniques and Applications Using Networked Workstations and Parallel Computers", Chapter 4, Prentice-Hall, Inc., 1999.	
		Baker, Lou et. al., "Parallel Programming", Chapter 4, McGraw Hill Companies, Inc., 1996.	
		Kayssi, A.; Harik, L.; Ferzli, R.; Fawaz, M; "FPGA-based Internet Protocol Firewall Chip"; Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Conference on Volume 1, 17-20 Dec. 2000 Page(s):316-319 vol. 1.[retrieved from IEEE database 6.9.08].	
		Office Action issued in Application No. 10/663,911 mailed September 26, 2008	
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